

# Design to conditioning circuits of dynamic compensation of reactive power in the intelligence voltage controller

Yong-qin WEI, Yin-jing GUO

(College of Infor. & Elec. Eng, Shan dong university of science of technology, Qingdao 266510, china)

**Abstract**—The paper introduces one design idea that making use of SCM to control Real-timely the dynamic compensation of reactive power. Firstly, design one Circuit to Sample the voltage and current, and by these datas we can easily calculate the power factor, and Voltage controller in the microcontroller to determine whether input the compensation capacitance according to the size of power factor, the paper also analyzes the principle of capacitance compensation and calculation method. Dynamic compensation for the entire process is quick and accurate.

**Keywords**—reactive power compensation; Real-timely control; dynamic; power factor

**Manuscript Number:** 1674-8042(2010)supp.-0111-03

**doi:** 10.3969/j.issn1674-8042.2010.supp..30

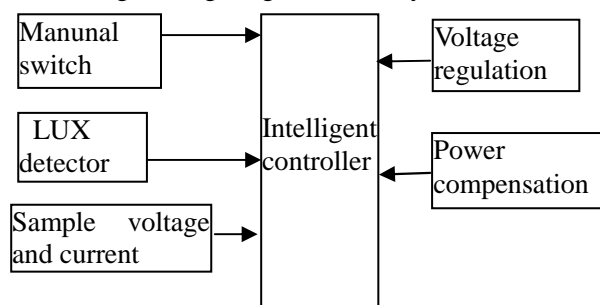
## 1 Introduction

At present, when the inductive load is less, there is comparatively mature intelligent control devices for lighting, which can implement the lighting of regulatory, effectively protect the electric light and reduce power consumption as so on. But in some lighting system, if using too many lamps which are inductive loading, Power system will be a great deal of reactive power, and make the network's power factor lower to affect the electrical power and network system's safety and function efficiently. So improve the network's power factor is very important, which can make the reactive power balanced and reduce the loss of wire, improve the quality of the voltage.

## 2 The structure of the intelligence voltage controller

We have studied the lighting systems and propose the thought of intelligent control on the base of regulating the voltage, the 51MCU is used as controller, the intelligent regulate voltage control system contains regulating voltages, sampling of the current and voltage, power compensation etc. This article will emphasize introduce the power compensation and voltage and current's sample. The sampling circuit

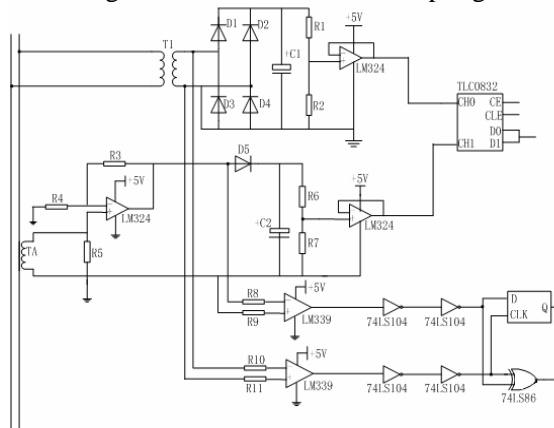
make the voltage and current and phase to digital form hand down MCU, according to the sample value and power factor, MCU forecast and control whether or not put into the C. figure 1 is the structure diagram of the intelligence lighting controller system:



**Fig.1** Intelligent lighting control system

## 3 The sample of voltage and current

The sampling circuit of voltage-current is shown in Fig2. It transmits the digital voltage, current and phase in circuit to the microcontroller, and the microcontroller carries out the forecast and control according to the sampling value.



**Fig.2** the sample circuit of voltage and current

The mutual inductor transforms the voltage and current of the lighting circuit to the secondary side, the two kinds signals are divided into two roads, one is A/D after amplification, rectification and follow by IN0 and IN1 channel of TLC0832, the other is sent to LM339 changing AC signal to square signal. The

**Received:** 2010-5-25

**Corresponding author:** Yong-qin Wei ([wei\\_yq75@sina.com](mailto:wei_yq75@sina.com))

square signals reflecting the line voltage and current phase are XOR by 74LS86, the width of output impulse is the phase difference of line voltage and current  $\Phi$ . Those square signals reflecting line voltage act as the clock of 74LS74, the other signals act as the input signals of 74LS74, the rising edge of D-flip flop is effective, when the output is low, it shows that the voltage is ahead of current, the lighting system is inductive loads, otherwise capacitive loads. If there is no impulse output in the XOR, the phase of voltage is the same as current, the system is resistive loads. The time of sampling circuit is controlled by microcontroller, by the way of interruption or search..

#### 4 Dynamic reactive compensation

When the lighting energy saving systems used inductive loads widely, there will be a lot of reactive powers; the power factor of power grid is extremely low. It influences the security and efficiency of power system. Therefore, improving the power factor to balance the reactive power, reduce the line loss, improve the quality of voltage is very important work.

Dynamic reactive compensation circuit consists mainly of fixed compensation capacitor, high-power SCR and an inverse parallel unsaturated reactor, as shown in figure 3 below.  $L_1$  and  $L_2$  is the inductance stopping the high frequency harmonics. Because the current flows by reactor are under the phase-shifting control of SCR, the susceptance of capacitance and inductance parallel branch varies from the range of capacitive segment and inductive segment. It can adjust the reactive power continuous supplied for power grid, completing dynamic stepless power factor compensation of lighting system. The shunt capacitance and inductance equivalent electric susceptance is:

$$b = j(\omega C - \frac{2\pi - 2\alpha + \sin 2\alpha}{\pi \omega L}) \quad (3.10)$$

$\alpha$  is the trigger angle of SCR. When  $\alpha = \pi / 2$  and  $1/\omega L > \omega C$ ,  $b = -j[(1/\omega L) - \omega C]$ , the circuit is inductive, parallel branch circuit gets the reactive power from the power grid. When  $\alpha = \pi$ ,  $b = j\omega C$ , the circuit is capacitive, parallel branch outputs reactive power to the power grid. Normally, parallel branch varies in the capacitive and inductive, and its electrical susceptance relates with control angles. If  $\cos \phi$  is low, the output signals of control device increase the SCR trigger angle, making parallel branch provide more reactive power demand of leading reactive power compensation inductive load to power grid, improving the power factor of load. Because the SCR trigger angle can change continuously, the compensation is dynamic.

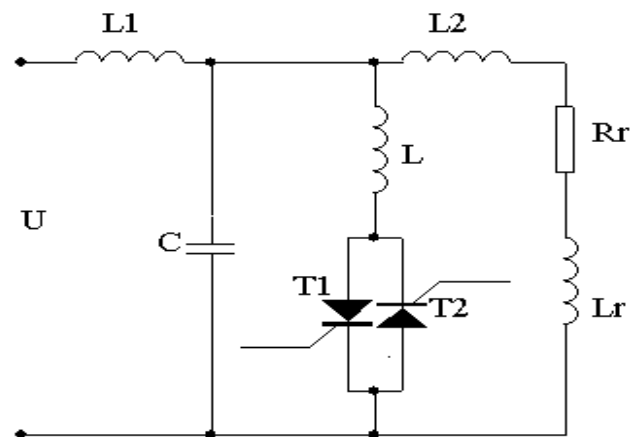


Fig. 3 schematic circuit diagram of dynamic reactive compensation

Capacitor C is used to compensate reactive power and improve the power factor. When lighting lamps in the stadium power-on, due to the lamps being inductive device, power factor in the circuit is lower. Take fluorescent light for example, power factor generally is 0.5 - 0.6. For the sodium lamp 400W, when the supply power is 220V, whose current is 4.1A, input apparent power is  $4.1 \times 220 = 902\text{VA}$ . But the actually active power is only about 480W, it shows the active power from the power grid is only 53.2%. if C was thrown into the circuit, the phase difference of the current lag behind the voltage will be decrease, so as to improve the power factor. The active power from the power grid will increase, This states that compensation device will greatly reduce the energy losses, and then saving energy. Dynamic reactive power compensation circuit is shown in Figure 4. For the capability of MCS-51 is limited, the output signal after amplification by 7407 and C filter is sent to the photocoupler, whose output is photosensitive bi-directional thyristor, but also equipped with zero-crossing detection circuit, used to control the thyristor trigger.

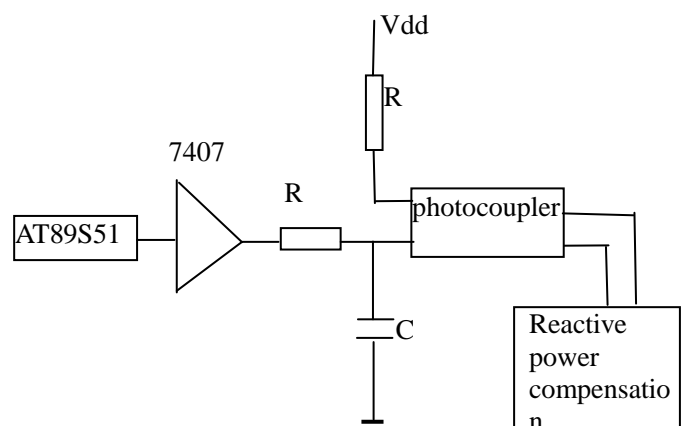


Fig.4 Dynamic reactive power compensation output circuit

The circuit is controlled by microcomputer. Sampling the load current, voltage and power factor, by ADC the datas are sent to the microcontroller, and according to the setting power factor values (eg 0.95), the MCU automatically control single thyristor trigger pulses to make power factor compensated. Microcontroller samples these parameters once every 30ms, and then calculate the instantaneous power:

$$P=3UI \cos\phi_1;$$

In the formula:  $U$  - load voltage, kV;

$I$  - Load Current, A;

$\cos\phi_1$  - instantaneous power factor.

To improve the power factor  $\cos\phi_1$  to  $\cos\phi_2$ , it requires compensation capacity of shunt capacitors:

$$Q_c = P(\tan\phi_1 - \tan\phi_2) = 3UI \cos\phi_1 (\tan\phi_1 - \tan\phi_2)$$

In the formula:  $\phi_2$  - the improved power factor angle.

Per phase needing capacitance of capacitor values, use triangle Connection

$$C = \frac{Q_c \times 10^3}{3U^2 \omega} \mu F$$

In the formula:  $U$  - the network line voltage, KV;  
 $\omega$  - angular frequency.

In the control processing, the MCU sampling load current, voltage and power factor per 30ms, when the power factor less than 0.95, immediately calculate the compensation capacitance required, and also instruct which groups of compensation capacitors to put into, thus it can be timely and accurately put the power

factor increased to 0.95. When the load power factor is greater than 0.99, the MCU immediately calculate the compensation capacitance to cut off, and give out instructions to remove which groups of compensation capacitor, so avoid going beyond, make the power factor less than 0.99.

The dynamic compensation process is fast and accurate. As the real-time control of microcomputer, the system power factor is will not be lacking, but also not go beyond.

## 5 Summary

From the last testing results of prototype, the dynamic reactive compensation is quick and accurate, and reach the expected purposes and demands.

## References

- [1] Chen Baiyao, Liu Qibin. Electrical Design and Intelligent of Gymnasium. China Illuminating Engineering Journal. 2002, 4: 45~47
- [2] Chen Tao, Mao Xinwei. Application of Intelligent Lighting Control System to the Engineering [J]. China Illuminating Engineering Journal 2001, 12(3): 49~54
- [3] Wu Yongqiao, Jin Kangjin. The design of SCM energy-efficient lighting control system. Global Electronics China. 2004, 4: 47~49
- [4] Wang Hailun, Ye Cunfen. Intelligent illumination control system controlled by MCU. 2005, 1: 121~122

### (From P.96)

From the above waveforms, the output result basically satisfies the design requirements and the amplitude can be controlled by the words rightly. The frequency values displayed on oscilloscope are shown as table 1:

**TABLE 1** THE OUTPUT FREQUENCIES

|               |         |           |
|---------------|---------|-----------|
| STANDARD DATA | 1Hz     | 100Hz     |
| MEASURED DATA | 1.001Hz | 100.025Hz |
| TOLERANCE     | ±3.5%   | ±3.5%     |

From the table 1, it is known that the output frequencies are correct.

## References

- [1] WU Liuzhen, YAN Junjuan, HAN Jisheng. TREATMENT ON 27 ADOLESCENTS WITH INTERNET ADDICTION BY 2/100 HZ HAN'S ACUPOINT NERVE STIMULATOR (HANS) [J]. CHINESE JOURNAL OF DRUG DEPENDENCE, 2007, 16(1): 32-35
- [2] Han JS, Wang Q. Mobilization of specific neuropeptides by peripheral stimulation of identified frequencies. News Physiol Sci, 2002, 7: 176-180.
- [3] Altera Corporation. Cyclone Devide Handbook. July, 2005.
- [4] FuYupeng, LiMinghao, LvJinhua. The Technique of DDS Design and its Realization with FPGA [J]. Journal of Dalian Nationalities University, 2004, 5: 46-47.
- [5] XiangXiaming, ZhangShouhong. Random Wave-generator Based on FPGA [J]. Modern Electronics Technique, 2004, 27(22): 8-10
- [6] Altera Corporation. DSP Builder User Guide [M]. Altera Corporation. 2005, 8.
- [7] PanSong, HuangJiye. EDA technology and VHDL [M]. Tsinghua University Press, 2007
- [8] JiangPing, WangJianxin, JiXunsheng. A Direct Digital Frequency Synthesizer Implemented with FPGA. [J], Electronic Engineer, 2002, 28(5): 43-47